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Application Number	10/628,601
Filing Date	July-28, 2003
First Named Inventor	Michael Berman
Group Art Unit	2812
Examiner Name	Olivia Luk
Attorney Docket Number	31892-70168 / 01-965/10

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Name	Registration Number
PETE P. SCOTT	33,279
SANDEEP JAGGI	43,331
TIMOTHY R. CROLL	36,771
LEO J. PETERS	33,562

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Telephone	408-433-7475	Fax	408-433-7460		

I am the:

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☒ Assignee of Record of the entire interest
Certificate under 37 CFR 3.73(b) is enclosed

SIGNATURE of Applicant or Assignee of Record

Name	SANDEEP JAGGI, Chief IP Counsel and Assistant Corporate Secretary, LSI Logic Corp.
Signature	
Date	7-19-04

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**In re Application of :**

Michael Berman et. al.

Serial No. : 10/628,601**Group Art Un** 2812**Filed :** July 28, 2003**Examiner:** Luk, Olivia**For :** Method And Apparatus For
Detecting Backside Contamination
During Fabrication Of A
Semiconductor Wafer**Attv Docket :** 31692-70168 / 01-965/1D

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to:
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Date Signature

CERTIFICATION UNDER 37 C.F.R. 3.73(b)

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P. O. Box 1450
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Sir:

LSI Logic Corporation, a Delaware corporation, certifies that it is the assignee of the entire right, title and interest in the patent application.

The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of the undersigned's knowledge and belief, title is in the assignee identified above.

The undersigned (whose title is supplied below) is empowered to act on behalf of the assignee.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

LSI Logic Corporation
1621 Barber Lane, MS D-106
Milpitas, CA 95035
(408) 433-7475

Date: 7-19-04

Respectfully submitted,

Sandeep Jaggi
Sandeep Jaggi
Chief IP Counsel
Corporate Assistant Secretary
LSI LOGIC CORPORATION

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CHANGE OF CORRESPONDENCE ADDRESS Application Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450	Application Number	10/628,601
	Filing Date	July-28, 2003
	First Named Inventor	Michael Berman
	Group Art Unit	2812
	Examiner Name	Olivia Luk
	Attorney Docket Number	31892-70188 / 01-965/1D

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Signature	
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